

Model Name: GA-H81M-D2V

www.xinxunwei.com 400-800-9990

Revision 1.02



SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS X1 *2 SLOT
16	PCI SLOT ( NA )
17	ITE 8620 LPC IO
18	COM,KB_MS_USB,USB30_20
19	HWM,FAN CTRL,OV
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC887-VD2
23	REAR AUDIO JACK
24	REALTEK RTL8111F
25	DISCRETE POWER
26	ATX
27	VCORE ISL95812_1

SHEET

TITLE

28	VCORE ISL95812_2
29	RT8120_DDR POWER
30	LPT
31	DVI
32	IT8892E ( NA )
33	USB3 VL805

Gigabyte Technology

Cover Sheet


Title	GA-H81M-D2V		
Size	Document Number	Rev	1.02
Custom			
Date:	Friday, November 08, 2013	Sheet	1 of 33

## Circuit or PCB layout change

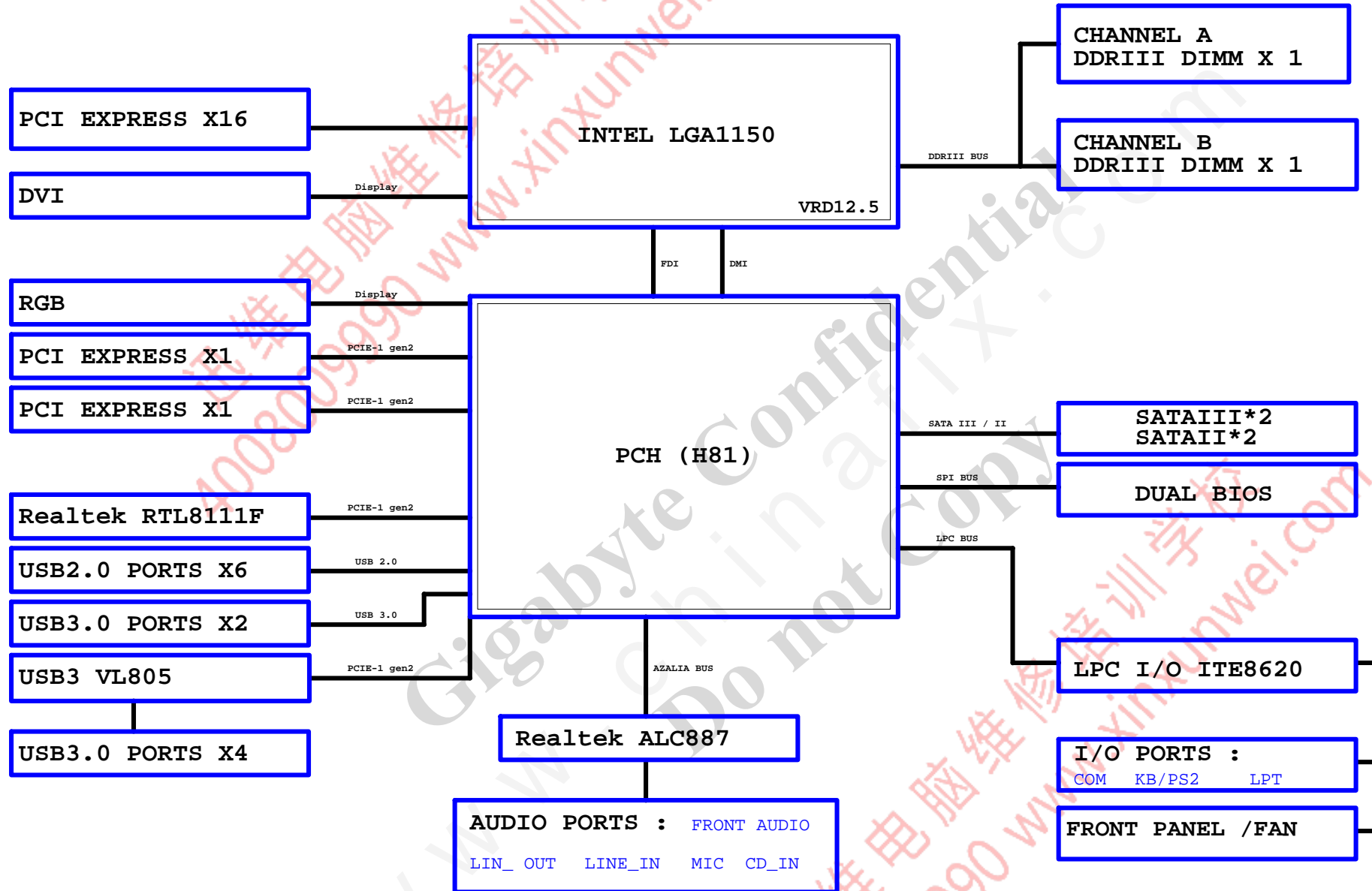
## 2013/04/08

[illegible]

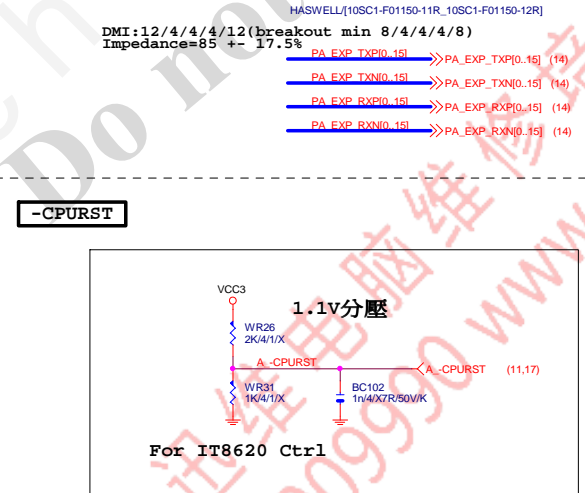
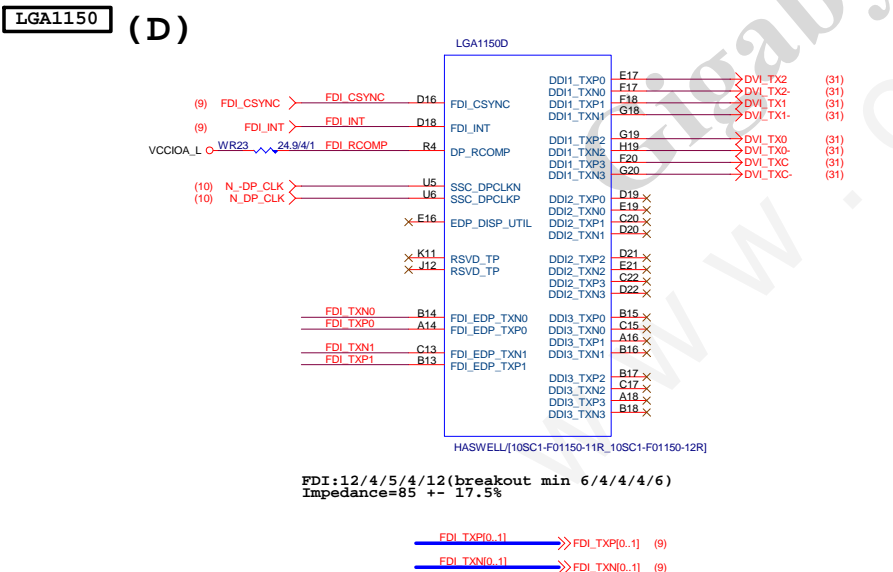
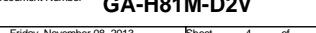
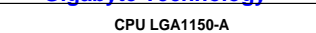
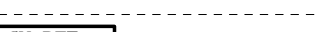
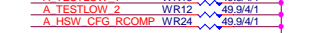
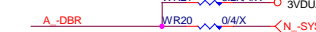
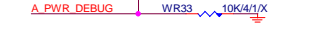
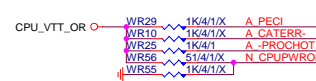
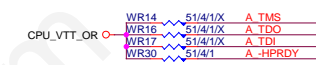
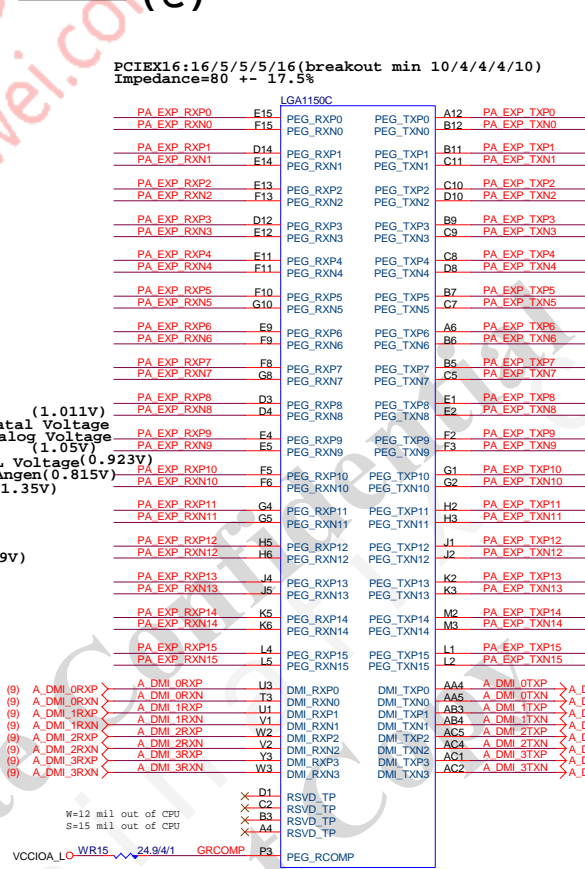
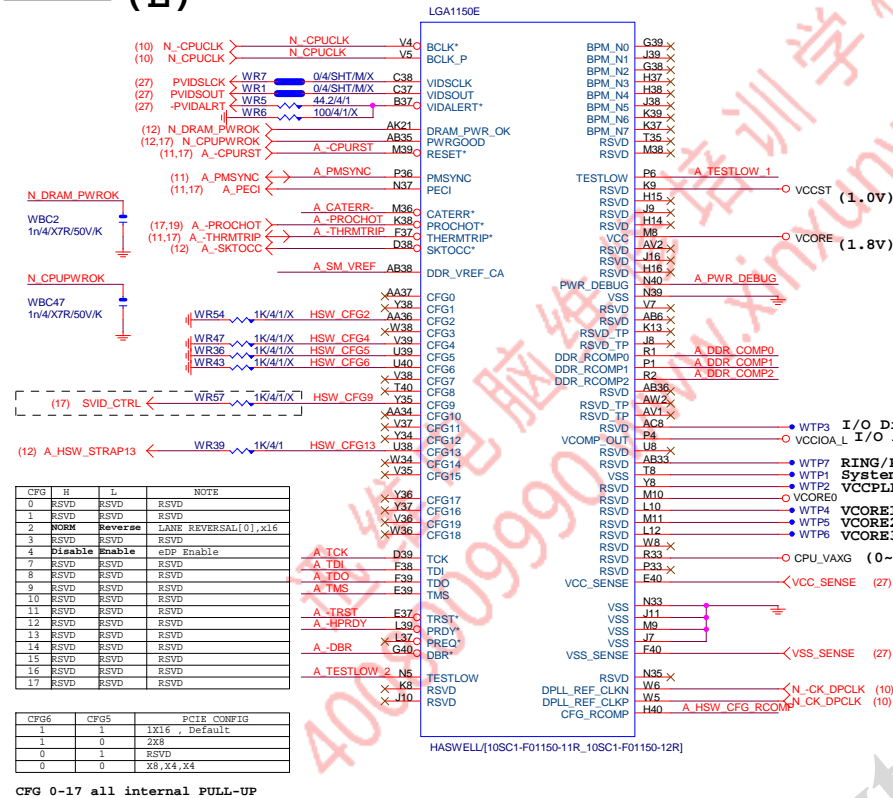
S:單文  
4:四層板  
V:第二層是VCC  
N:咖啡色  
B:製程

<div style="text-align: center;">  </div>			
Title			
BOM & PCB MODIFY HISTORY			
Size Custom	Document Number	GA-H81M-D2V	Rev 1.02
Date:	Friday, November 08, 2013	Sheet	2 of 33

## BLOCK DIAGRAM



## Gigabyte Technology



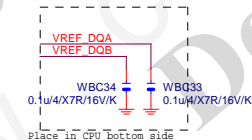




LGA1150A

MAAA0	AU13	DDR0_MA0	DDR0_D00	AD38	MDA0
MAAA1	AV16	DDR0_MA1	DDR0_D01	AD39	MDA1
MAAA2	AV16	DDR0_MA2	DDR0_D02	AF38	MDA2
MAAA3	AW17	DDR0_MA3	DDR0_D03	AF39	MDA3
MAAA4	AU17	DDR0_MA4	DDR0_D04	AD37	MDA4
MAAA5	AW18	DDR0_MA5	DDR0_D05	AD40	MDA5
MAAA6	AW17	DDR0_MA6	DDR0_D06	AE37	MDA6
MAAA7	AT18	DDR0_MA7	DDR0_D07	AF40	MDA7
MAAA8	AU18	DDR0_MA8	DDR0_D08	AH40	MDA9
MAAA9	AT19	DDR0_MA9	DDR0_D09	AH39	MDA10
MAAA10	AW11	DDR0_MA10	DDR0_D10	AK38	MDA10
MAAA11	AV19	DDR0_MA11	DDR0_D11	AK39	MDA11
MAAA12	AU19	DDR0_MA12	DDR0_D12	AH37	MDA12
MAAA13	AY10	DDR0_MA13	DDR0_D13	AH38	MDA8
MAAA14	AT20	DDR0_MA14	DDR0_D14	AK37	MDA14
MAAA15	AU21	DDR0_MA15	DDR0_D15	AK40	MDA15
MODT_A0	AW10	DDR0_ODT0	DDR0_D16	AM40	MDA17
MODT_A1	AY8	DDR0_ODT1	DDR0_D17	AP38	MDA21
AW8		DDR0_ODT2	DDR0_D18	AP39	MDA19
AW8		DDR0_ODT3	DDR0_D19	AM37	MDA20
AW33			DDR0_D20	AM38	MDA16
AW33			DDR0_D21	AP47	MDA22
AW33			DDR0_D22	AP40	MDA23
AW33			DDR0_D23	AV37	MDA25
AW33			DDR0_D24	AW37	MDA29
AW33			DDR0_D25	AU35	MDA26
AW33			DDR0_D26	AV35	MDA27
AW33			DDR0_D27	AT37	MDA28
AW33			DDR0_D28	AU37	MDA24
AW33			DDR0_D29	AT35	MDA30
AW33			DDR0_D30	AW35	MDA31
AW33			DDR0_D31	AY8	MDA33
AW33			DDR0_D32	AU8	MDA37
AW33			DDR0_D33	AV4	MDA34
AW33			DDR0_D34	AU4	MDA35
AW33			DDR0_D35	AW6	MDA36
AW33			DDR0_D36	AW6	MDA32
AW33			DDR0_D37	AW4	MDA38
AW33			DDR0_D38	AY4	MDA39
AW33			DDR0_D39	AR1	MDA41
AW33			DDR0_D40	AR4	MDA45
AW33			DDR0_D41	AN3	MDA42
AW33			DDR0_D42	AN4	MDA43
AW33			DDR0_D43	AR2	MDA44
AW33			DDR0_D44	AR3	MDA40
AW33			DDR0_D45	AN2	MDA46
AW33			DDR0_D46	AN1	MDA47
AW33			DDR0_D47	AL1	MDA49
AW33			DDR0_D48	AL4	MDA53
AW33			DDR0_D49	AL4	MDA50
AW33			DDR0_D50	AJ4	MDA51
AW33			DDR0_D51	AL2	MDA52
AW33			DDR0_D52	AL3	MDA48
AW33			DDR0_D53	AJ2	MDA54
AW33			DDR0_D54	AJ1	MDA55
AW33			DDR0_D55	AG1	MDA57
AW33			DDR0_D56	AG4	MDA61
AW33			DDR0_D57	AE3	MDA58
AW33			DDR0_D58	AE4	MDA59
AW33			DDR0_D59	AG2	MDA60
AW33			DDR0_D60	AG3	MDA56
AW33			DDR0_D61	AE2	MDA62
AW33			DDR0_D62	AE1	MDA63
AW33			DDR0_D63	AE39	DQSA0
AW33			DDR0_D64	AJ39	DQSA1
AW33			DDR0_D65	AN39	DQSA2
AW33			DDR0_D66	AV36	DQSA3
AW33			DDR0_D67	AV5	DQSA4
AW33			DDR0_D68	AP3	DQSA5
AW33			DDR0_D69	AK3	DQSA6
AW33			DDR0_D70	AF3	DQSA7
AW33			DDR0_D71	AV32	DQSA0
AW33			DDR0_D72	AE38	DQSA1
AW33			DDR0_D73	AJ38	DQSA2
AW33			DDR0_D74	AN38	DQSA3
AW33			DDR0_D75	AJ36	DQSA4
AW33			DDR0_D76	AW5	DQSA5
AW33			DDR0_D77	AP2	DQSA6
AW33			DDR0_D78	AK2	DQSA7
AW33			DDR0_D79	AF2	DQSA7
AW33			DDR0_D80	AJ32	

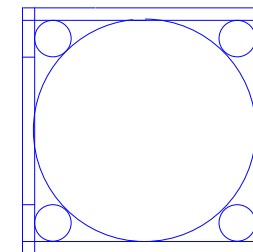
HASWELL[10SC1-F01150-11R\_10SC1-F01150-12R]



LGA1150B

MAAB0	AL19	DDR1_MA0	AE34	MD80
MAAB1	AK23	DDR1_MA1	AE35	MD81
MAAB2	AM22	DDR1_MA2	AG35	MD82
MAAB3	AM23	DDR1_MA3	AH35	MD83
MAAB4	AP23	DDR1_MA4	AD34	MD84
MAAB5	AL23	DDR1_MA5	AD35	MD85
MAAB6	AY24	DDR1_MA6	AG34	MD86
MAAB7	AV25	DDR1_MA7	AH34	MD87
MAAB8	AU26	DDR1_MA8	AL34	MD88
MAAB9	AW25	DDR1_MA9	AL35	MD89
MAAB10	AP18	DDR1_MA10	AL31	MD810
MAAB11	AK38	DDR1_MA11	AL31	MD811
MAAB12	AV28	DDR1_MA12	AK34	MD812
MAAB13	AR15	DDR1_MA13	AK35	MD813
MAAB14	AV27	DDR1_MA14	AK32	MD814
MAAB15	AV28	DDR1_MA15	AL32	MD815
MODT_B0	AM17	DDR1_ODT0	AP34	MD817
MODT_B1	AL16	DDR1_ODT1	AN31	MD819
AM16		DDR1_ODT2	AP31	MD823
AK15		DDR1_ODT3	AP35	MD820
AM26		DDR1_ECC0	AP35	MD816
AM25		DDR1_ECC1	AN32	MD818
AP25		DDR1_ECC2	AP32	MD822
AP28		DDR1_ECC3	AM29	MD825
AL26		DDR1_ECC4	AM28	MD828
AL25		DDR1_ECC5	AR29	MD827
AR26		DDR1_ECC6	AR28	MD830
AR26		DDR1_ECC7	AL23	MD834
AK17		DDR1_BA0	AL28	MD829
AL18		DDR1_BA1	AP29	MD826
AW28		DDR1_BA2	AP28	MD831
AW29		DDR1_CKE0	AR12	MD832
AW29		DDR1_CKE1	AL12	MD833
AW29		DDR1_CKE2	AL13	MD834
AW29		DDR1_CKE3	AL12	MD835
AP17		DDR1_CS_N0	AR13	MD836
AN15		DDR1_CS_N1	AP13	MD837
AN17		DDR1_CS_N2	AM13	MD838
AL15		DDR1_CS_N3	AM12	MD839
AM20		DDR1_CLK_P0	AR9	MD845
AM21		DDR1_CLK_P1	AP9	MD841
AP22		DDR1_CLK_P2	AR6	MD847
AP21		DDR1_CLK_P3	AP6	MD843
AN20		DDR1_CLK_P4	AR10	MD844
AN21		DDR1_CLK_P5	AP10	MD840
AP20		DDR1_CLK_P6	AR7	MD846
AP20		DDR1_CLK_P7	AP7	MD842
AP16C		DDR1_CAS*	AM9	MD852
AL20		DDR1_RAS*	AL9	MD853
AK16C		DDR1_WE*	AL6	MD850
AP16C		DDR1_VREF_DOA	AM10	MD848
AP16C		DDR1_VREF_DOB	AL10	MD849
AP16C		DDR1_VREF_DOB	AM6	MD854
AP16C		DDR1_VREF_DOB	AM2	MD851
AP16C		DDR1_VREF_DOB	AH6	MD861
AP16C		DDR1_VREF_DOB	AH7	MD860
AP16C		DDR1_VREF_DOB	AE6	MD859
AP16C		DDR1_VREF_DOB	AE7	MD863
AP16C		DDR1_VREF_DOB	AJ6	MD856
AP16C		DDR1_VREF_DOB	AJ7	MD857
AP16C		DDR1_VREF_DOB	AF6	MD858
AP16C		DDR1_VREF_DOB	AF7	MD862
AP16C		DDR1_VREF_DOB	AF35	DQSB0
AP16C		DDR1_VREF_DOB	AL33	DQSB1
AP16C		DDR1_VREF_DOB	AP33	DQSB2
AP16C		DDR1_VREF_DOB	AN28	DQSB3
AP16C		DDR1_VREF_DOB	AN12	DQSB4
AP16C		DDR1_VREF_DOB	AP8	DQSB5
AP16C		DDR1_VREF_DOB	AL8	DQSB6
AP16C		DDR1_VREF_DOB	AG7	DQSB7
AP16C		DDR1_VREF_DOB	AN25	
AP16C		DDR1_VREF_DOB	AK33	DQSB0
AP16C		DDR1_VREF_DOB	AN33	DQSB2
AP16C		DDR1_VREF_DOB	AN29	DQSB3
AP16C		DDR1_VREF_DOB	AN13	DQSB4
AP16C		DDR1_VREF_DOB	AR8	DQSB5
AP16C		DDR1_VREF_DOB	AM8	DQSB6
AP16C		DDR1_VREF_DOB	AG6	DQSB7
AP16C		DDR1_VREF_DOB	AN28	

HASWELL[10SC1-F01150-11R\_10SC1-F01150-12R]

CR  
CPU RETENTION/X

LGA1150



ILM\_BP/1156/CSP/ILM\_BP/1156/CSP/[12KRC-0F0001-52R\_12KRC-0F0001-51R]

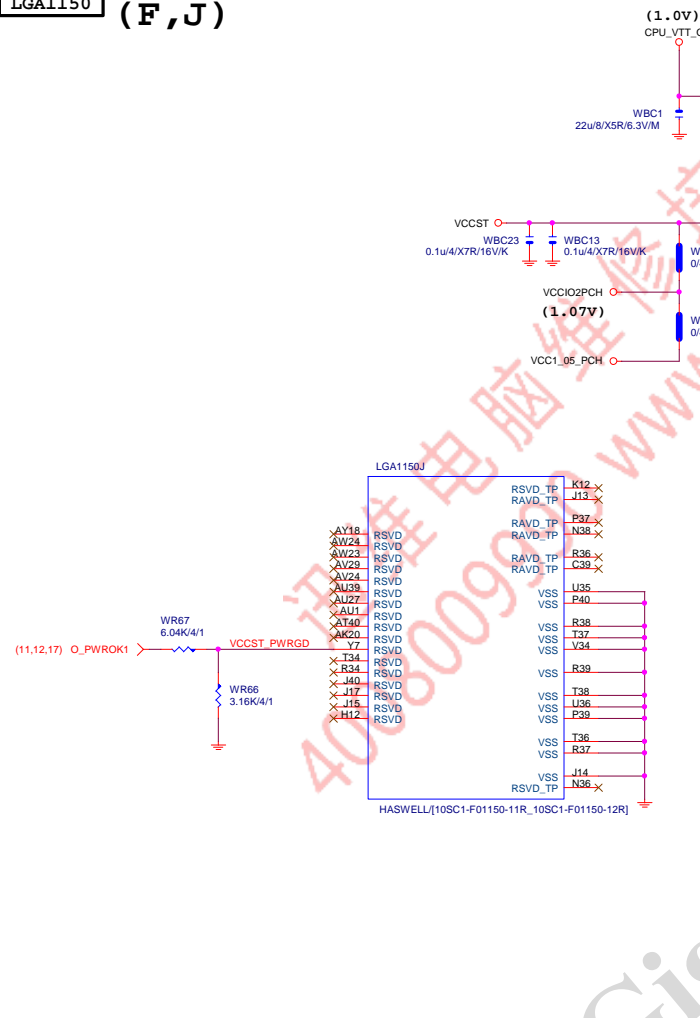
DDR BUS

(7) MODT_A[0..1]	MODT_A0..1
(8) MODT_B[0..1]	MODT_B0..1
(7) MDA[0..63]	MDA0..63
(8) MDB[0..63]	MDB0..63
(7) DQSA[0..7]	DQSA0..7
(7) DQSA[0..7]	DQSA0..7
(7) MAA[0..15]	MAA0..15
(8) MAB[0..15]	MAB0..15
(8) DQSB[0..7]	DQSB0..7
(8) DQSB[0..7]	DQSB0..7

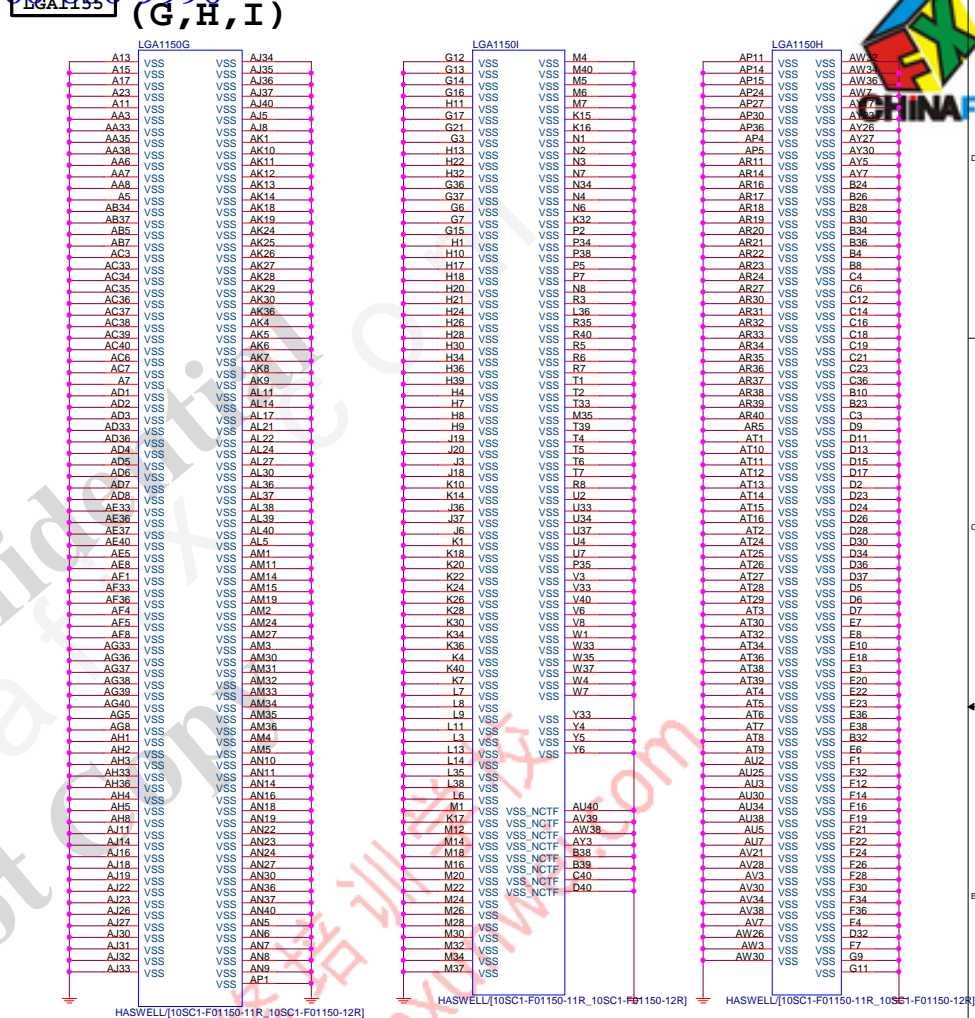
## Gigabyte Technology

Title			CPU LGA1150-B
Size			GA-H81M-D2V
Date			Friday, November 08, 2013
Sheet			5 of 33

# LGA1150 (F,J)

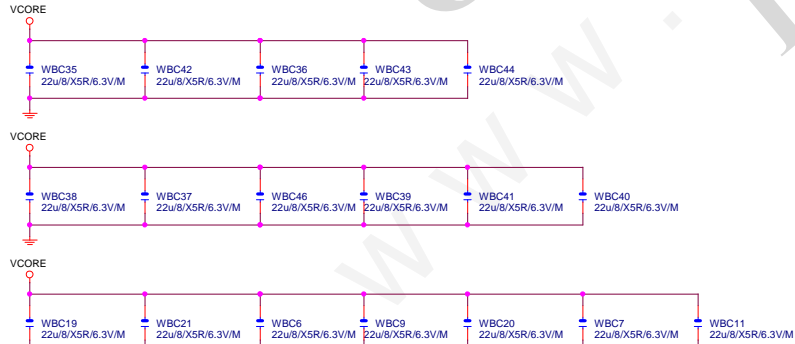


# LGA1150 (G,H,I)



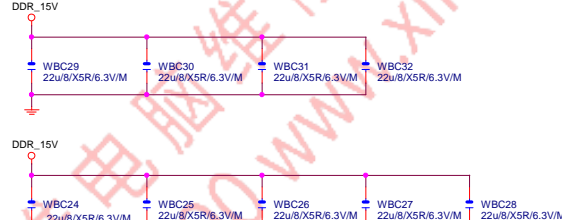
## VCore CAP

(X18)



## DDR CAP

(X9)



Gigabyte Technology

Title				
CPU LGA1150-C				
Size	Document Number	GA-H81M-D2V		Rev
Custom				1.02
Date:	Friday, November 08, 2013	Sheet	6	of 33







## PCH (B)

DMI:12/4/4/4/12(breakout min 8/4/4/4/8)  
Impedance=85 +- 17.5%

(4) A\_DMI\_0TXN A\_DMI\_0TXN L24  
(4) A\_DMI\_0TXP A\_DMI\_0TXP K24  
(4) A\_DMI\_0RXN A\_DMI\_0RXN C20  
(4) A\_DMI\_0RXP A\_DMI\_0RXP B20  
(4) A\_DMI\_1TXN A\_DMI\_1TXN G24  
(4) A\_DMI\_1TXP A\_DMI\_1TXP H24  
(4) A\_DMI\_1RXN A\_DMI\_1RXN B21  
(4) A\_DMI\_1RXP A\_DMI\_1RXP B21  
(4) A\_DMI\_2TXN A\_DMI\_2TXN F26  
(4) A\_DMI\_2TXP A\_DMI\_2TXP G26  
(4) A\_DMI\_2RXN A\_DMI\_2RXN B22  
(4) A\_DMI\_2RXP A\_DMI\_2RXP C22  
(4) A\_DMI\_3TXN A\_DMI\_3TXN K26  
(4) A\_DMI\_3TXP A\_DMI\_3TXP L26  
(4) A\_DMI\_3RXN A\_DMI\_3RXN A24  
(4) A\_DMI\_3RXP A\_DMI\_3RXP B24

W=4 mil out of PCH  
S=15 mil out of PCH

VCC1\_5\_PCH NR50 7.5K/4/1 DMI\_COMP B19  
NR40 7.5K/4/1 PCIE\_COMP C13  
CK\_SRCCLK\_PCH G22  
CK\_SRCCLK\_PCH F22

## PCIE Only

8111G [ (24) LA\_ML\_IN (24) LA\_ML\_IP (24) LA\_ML\_ON (24) LA\_ML\_OP (33) USB3\_IN1 (33) USB3\_IN1 (33) USB3\_ON1 (33) USB3\_OP1 (15) PL\_PCIE1\_IN (15) PL\_PCIE1\_IP (15) PL\_PCIE1\_ON (15) PL\_PCIE1\_OP (15) PJ\_PCIE1\_IN (15) PJ\_PCIE1\_IP (15) PJ\_PCIE1\_ON (15) PJ\_PCIE1\_OP

N/A

放靠近 Device & PCI-E Slot  
Impedance=80 +- 17.5%

PCIE1:16/5/5/5/16 (breakout min 8/4/4/4/8)

## PCH (J)

PCHJ  
AT1 VSS\_NCTF TP22 U11  
AT41 VSS\_NCTF TP23 U10  
AU1 VSS\_NCTF TP21 AJ14  
AV1 VSS\_NCTF TP20 AK14  
AV2 VSS\_NCTF TP14 K34  
AV40 VSS\_NCTF TP15 K33  
AV41 VSS\_NCTF TP12 AH24  
AW2 VSS\_NCTF TP10 L16  
AW40 VSS\_NCTF TP11 K16  
B40 VSS\_NCTF TP9 AM34  
B41 VSS\_NCTF TP3 R12  
C41 VSS\_NCTF TP4 N12  
D1 VSS\_NCTF TP1 L22  
D41 VSS\_NCTF TP2 K22  
TP5 R4  
TP6 K5  
TP7 P5  
TP8 L5  
VSS AC31  
VSS AF3  
VSS AV21

H81/S

USB2.0 : 12/4.5/7.5/4.5/12 (breakout min 8/4/4/4/8)  
Impedance=90 +- 17.5%

PCHB

B85: Port 6/7 N/A  
H81: Port 6/7/12/13 N/A

USBN\_0 AV10 N-USBP0 N-USBP0 (21)  
USBP\_0 AU10 N-USBP0 N-USBP0 (21)  
USBN\_1 AV11 N-USBP1 N-USBP1 (21)  
USBP\_1 AW11 N-USBP1 N-USBP1 (21)  
USBN\_2 AN14 N-USBP2 N-USBP2 (21)  
USBP\_2 AP14 N-USBP2 N-USBP2 (21)  
USBN\_3 AK16 N-USBP3 N-USBP3 (21)  
USBP\_3 AU16 N-USBP3 N-USBP3 (21)  
USBN\_4 AV15 N-USBP4 N-USBP4 (18)  
USBP\_4 AU15 N-USBP4 N-USBP4 (18)  
USBN\_5 AU12 N-USBP5 N-USBP5 (18)  
USBP\_5 AT12 N-USBP5 N-USBP5 (18)  
USBN\_6 AV14 N-USBP6 N-USBP6 (21)  
USBP\_6 AU14 N-USBP6 N-USBP6 (21)  
USBN\_7 AU17 N-USBP7 N-USBP7 (21)  
USBP\_7 AW17 N-USBP7 N-USBP7 (21)  
USBN\_8 AV16 N-USBP8 N-USBP8 (21)  
USBP\_8 AU16 N-USBP8 N-USBP8 (21)  
USBN\_9 AN16 N-USBP9 N-USBP9 (21)  
USBP\_9 AP16 N-USBP9 N-USBP9 (21)  
USBN\_10 AJ18 N-USBP10 N-USBP10 (21)  
USBP\_10 AK18 N-USBP10 N-USBP10 (21)  
USBN\_11 AP18 N-USBP11 N-USBP11 (21)  
USBP\_11 AN18 N-USBP11 N-USBP11 (21)  
USBN\_12 AV18 N-USBP12 N-USBP12 (21)  
USBP\_12 AU18 N-USBP12 N-USBP12 (21)  
USBN\_13 AP20 N-USBP13 N-USBP13 (21)  
USBP\_13 AN20 N-USBP13 N-USBP13 (21)

H81: Port 6/7/12/13 N/A

OC0B\_GP59 AE40 N-USBOC\_F (18,21)  
OC1B\_GP40 AE37 N-USBOC\_R (18)  
OC2B\_GP41 AD39  
OC3B\_GP42 AD40  
OC4B\_GP43 AE39  
OC5B\_GP9 AC41  
OC6B\_GP10 AC40 N\_GPIO14  
OC7B\_GP14 AG40

USBRBIASB NR47 22.6/4/1  
USBRBIASB AU20  
AP11 CK\_DOTCLK  
AM11 CK\_DOTCLK

NR130 8.2K/4  
N\_GPIO14 0.3V DUAL  
NBC82 0.1u/4/X7R/16V/K  
NBC83 0.1u/4/X7R/16V/K

N-USBOC\_F  
N-USBOC\_R

## PCH (F)

(21) PCH\_USB3\_RXN0 F20 USB3\_RXN\_0  
(21) PCH\_USB3\_RXP0 G20 USB3\_RXP\_0  
(21) PCH\_USB3\_TXN0 B18 FDI\_RXN\_0  
(21) PCH\_USB3\_TXP0 C18 USB3\_TXP\_0  
(21) PCH\_USB3\_RXN1 G18 USB3\_RXN\_1  
(21) PCH\_USB3\_RXP1 H18 USB3\_RXP\_1  
(21) PCH\_USB3\_TXN1 B15 FDI\_TXN\_1  
(21) PCH\_USB3\_TXP1 B16 USB3\_TXP\_1

N/A

VCC3 NR62 8.2K/4 AK28  
NR63 8.2K/4 AT34

PCHF

USB3 FDI LINK  
USB3\_RXN\_0 FDI\_RXN\_0 N1 FDI\_TXN0  
USB3\_RXP\_0 FDI\_RXP\_0 N2 FDI\_TXP0  
USB3\_TXN\_0 FDI\_TXN\_1 P2 FDI\_TXN1  
USB3\_TXP\_0 FDI\_TXP\_1 P3 FDI\_TXP1

USB3\_RXN\_1 FDI\_CSXNC L2 FDI\_CSXNC (4)  
USB3\_RXP\_1 FDI\_CSXNC L2 FDI\_CSXNC (4)  
USB3\_TXN\_1 FDI\_INT L3 FDI\_INT (4)  
USB3\_TXP\_1 FDI\_INT L3 FDI\_INT (4)

USB3\_RXN\_4 USB3\_RXP\_4 K2 NR29 7.5K/4/1 VCC1\_5\_PCH  
USB3\_TXN\_4 USB3\_TXP\_4

USB3\_RXN\_5 USB3\_RXP\_5  
USB3\_TXN\_5 USB3\_TXP\_5

TACH6\_GP70  
TACH7\_GP71

H81/S

FDI\_TXP0\_11 FDI\_TXP0\_11 (4)

FDI\_TXN0\_11 FDI\_TXN0\_11 (4)

USB3.0:20/5/7/5/20 (breakout min 8/4/4/4/8) ; ONLY 3 VIAS  
Impedance=85 +- 17.5%  
Back Panel < 10000 MILS  
Front Panel < 6000 MILS

## PCH CLK PD

CK\_SRCCLK\_PCH NR89 8.2K/4  
CK\_SRCCLK\_PCH NR88 8.2K/4

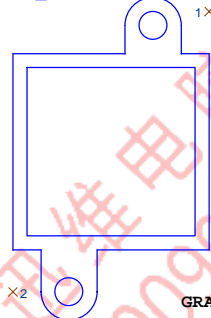
Mount for integrated clock Generation Mode

CK\_DOTCLK NR92 8.2K/4  
CK\_DOTCLK NR91 8.2K/4  
NR225 short to GND in non graphic SKU

## PCH H/S

## LOW COST ICH7 HEATSINK

SB\_HEATSINK



PCH\_HS  
PCH\_HS[12SP2-030005-41R]

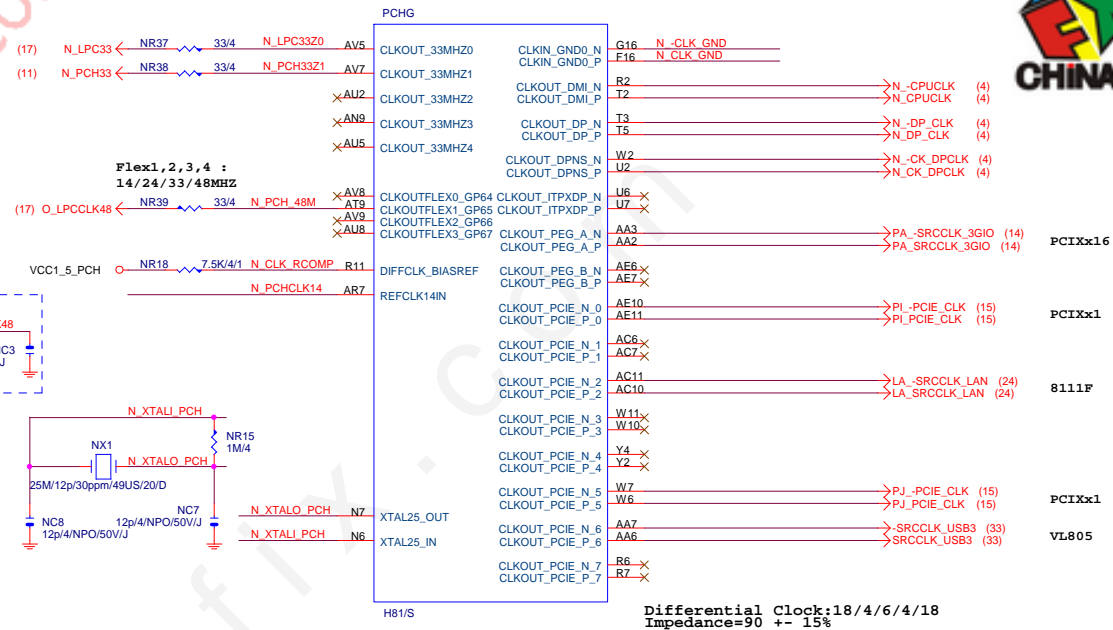
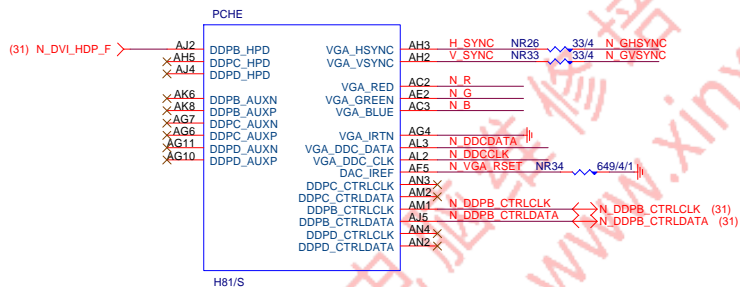
## USB TABLE

OC[3:0]# for Device 29 (ports 0-7)  
OC[7:4]# for Device 26 (ports 8-13)

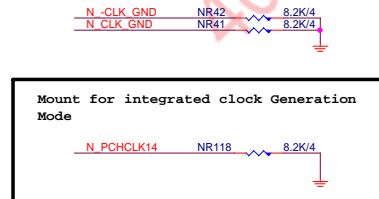
USB OC#	Configure
OC0#	F_USB30
OC1#	USB_LAN
OC2#	R_USB30
OC3#	N/A
OC4#	F_USB1
OC5#	F_USB2
OC6#	KB_MS_USB
OC7#	Not Use

## Gigabyte Technology

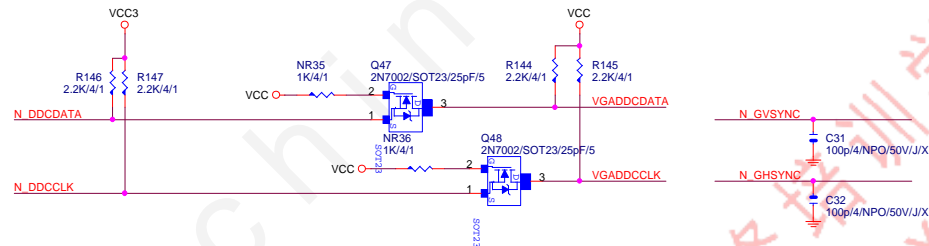
Title	PCH FDI,DMI,USB,PCIE,NVRAM		
Size	Document Number	GA-H81M-D2V	Rev 1.02
Custom			
Date:	Friday, November 08, 2013	Sheet 9 of 33	1



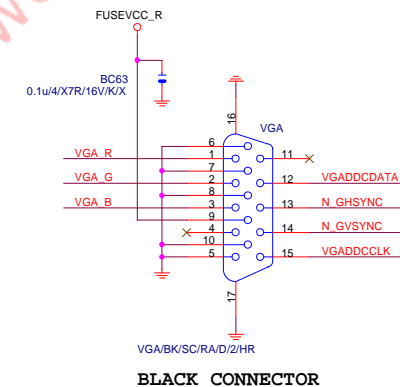
## PCH CLK PD



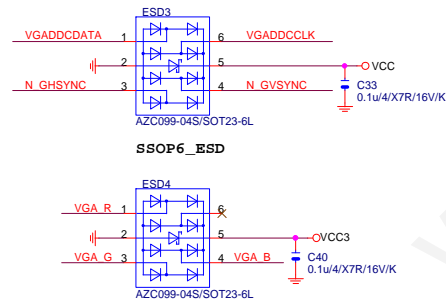
## VGA DDC



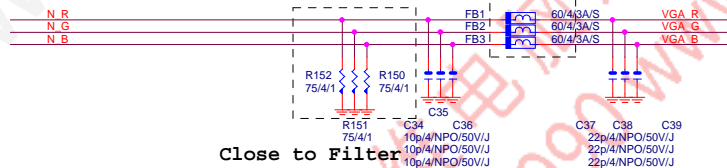
## VGA CONNECTOR



## VGA ESD



## VGA DDC



Gigabyte Technology

PCH DISPLAY,CLK BUFFER

GA-H81M-D2V

Rev 1.02

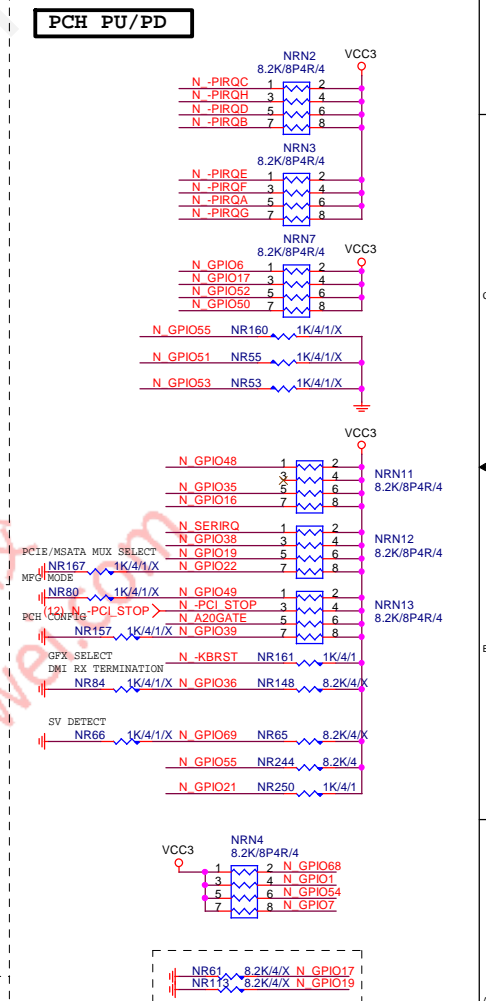
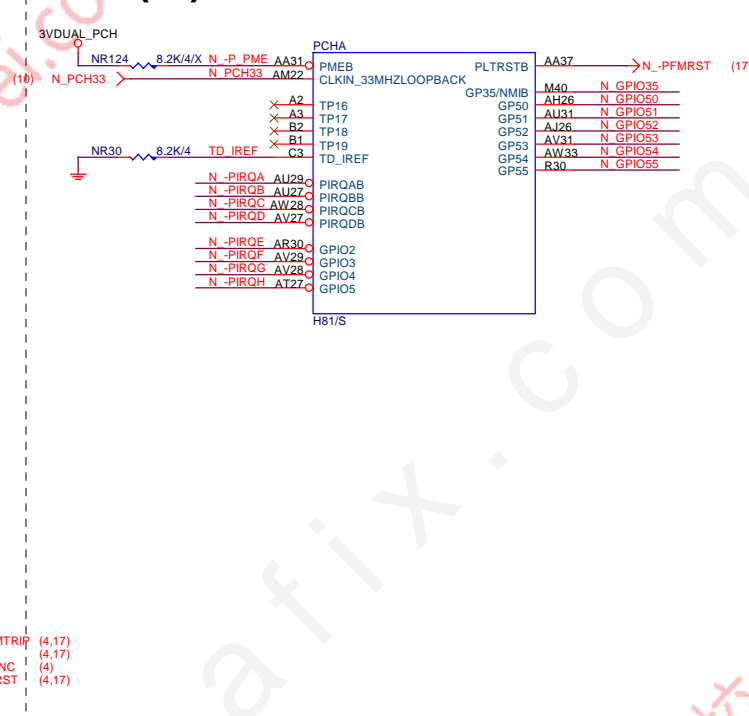
Title

Size Custom

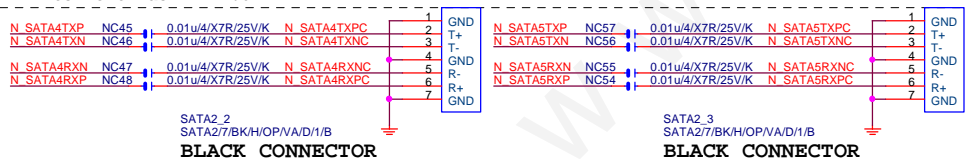
Date: Friday, November 08, 2013

Document Number

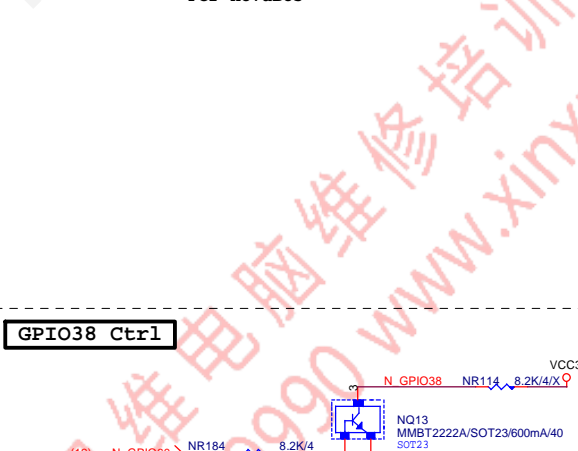
Sheet 10 of 33



```
** Z87/H87 Port 4&5 SATA3.0
** B85 Port 4&5 SATA2.0
```

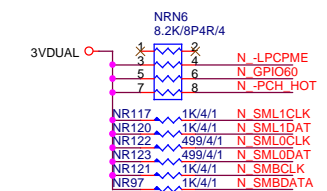
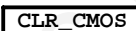
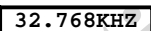
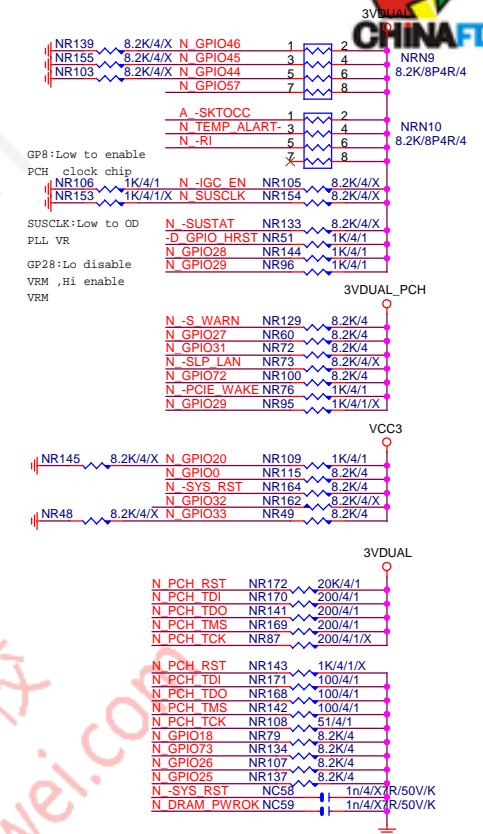
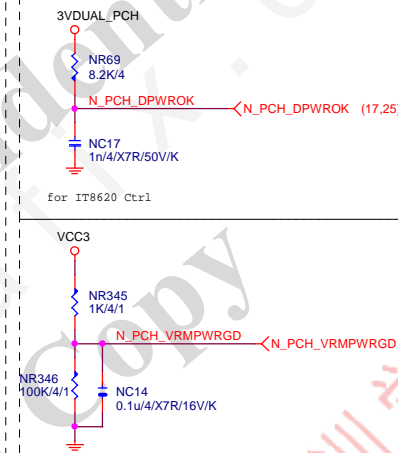


GPI037 PU VCC3 ENABLE SBA  
For H87&B85



GPI038 Ctrl





## Gigabyte Technology

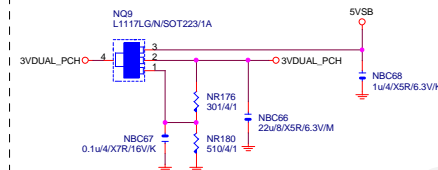
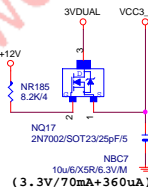
PCH GPIO , CTRL , AUDIO

GA-H81M-D2V

1.02



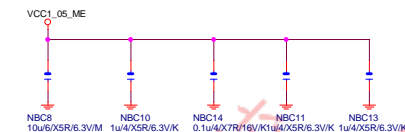
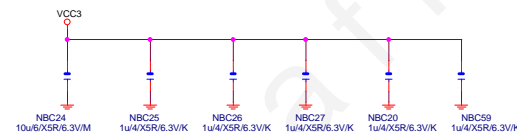
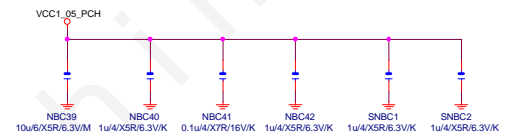
SHT PWR



VCC3\_ME    VCC3

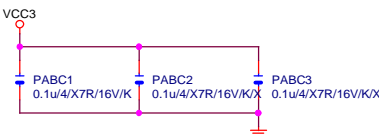
VCC1\_05\_ME    VCC1\_05\_PCH

(1.05V) (x5)

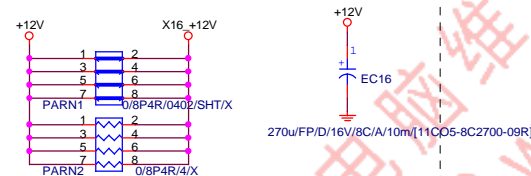

$$(1.05V)(x2)(3.3V)(x2)$$
[illegible][illegible]



## PCIEX16 CAP



## PCIEX16 PROTECT SHT



## PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXP0.15] >>> PA\_EXP\_RXP[0.15] (4)

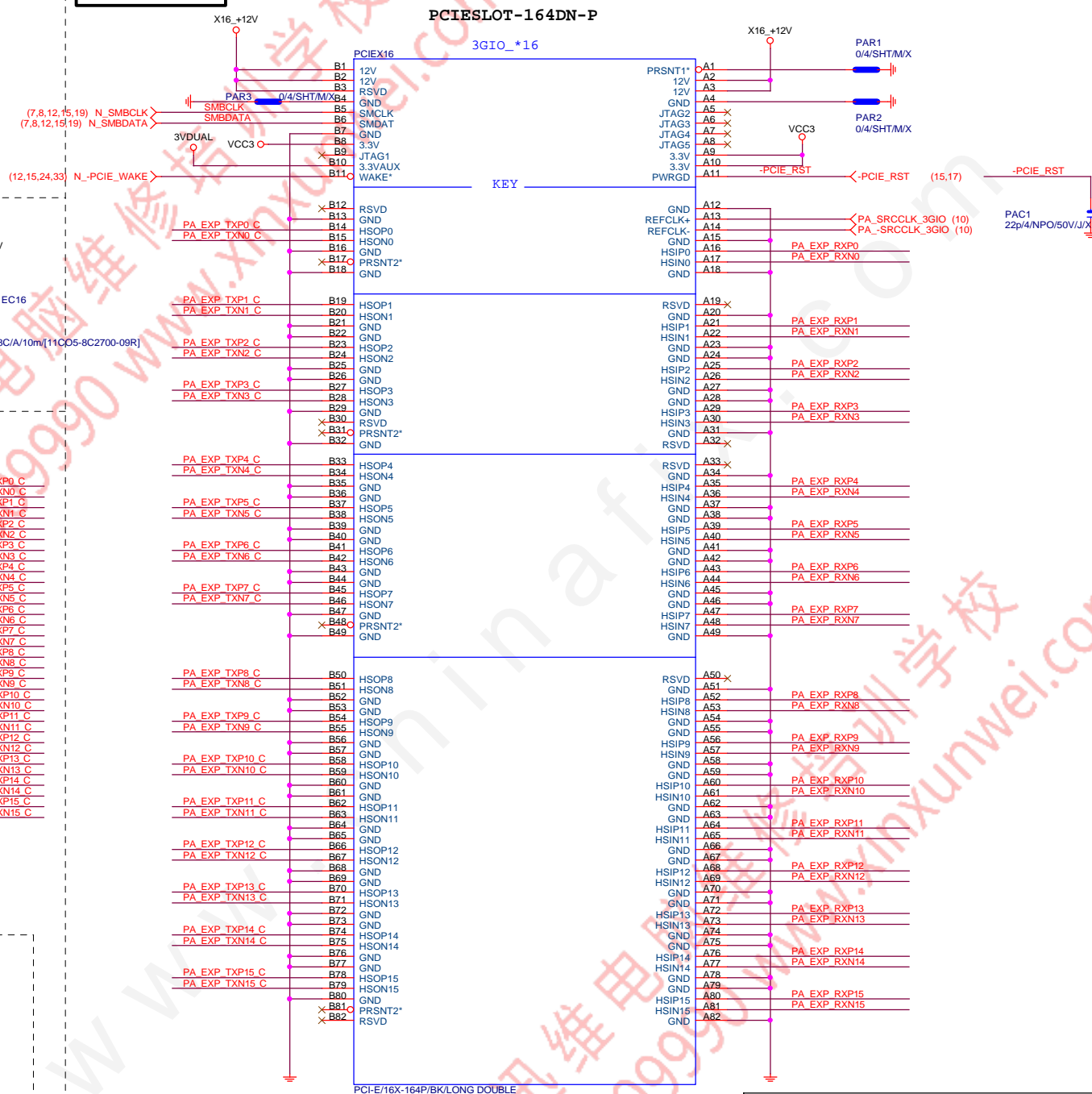
PA EXP RXN0.15] >>> PA\_EXP\_RXN[0.15] (4)

PA EXP TXP0.15] >>> PA\_EXP\_TXP[0.15] (4)

PA EXP TXN0.15] >>> PA\_EXP\_TXN[0.15] (4)

The auxiliary reset circuit is only required for PCIe Gen3 margining and functional link training

## PCIEX16 SLOT

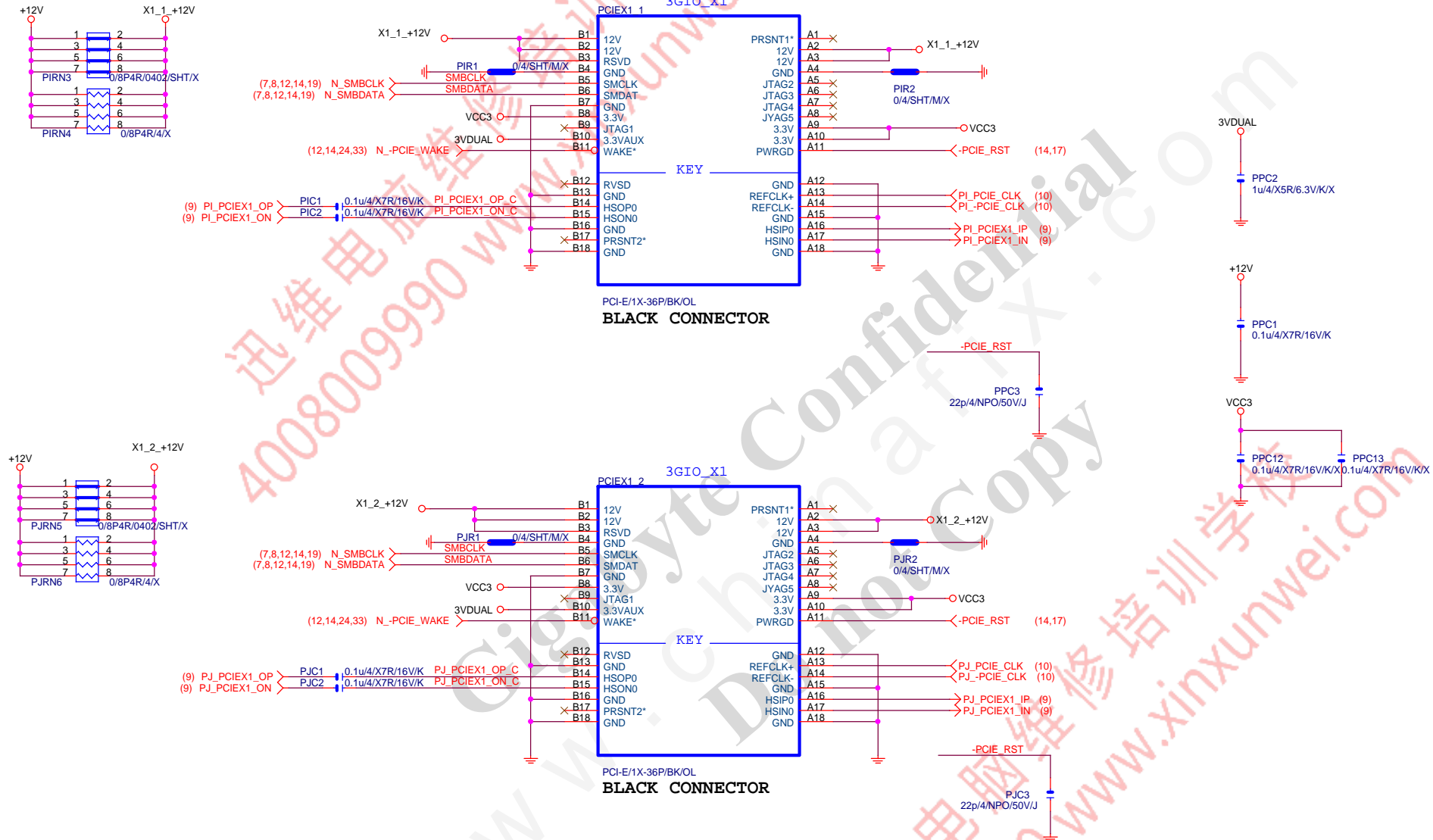


BLACK CONNECTOR

Gigabyte Technology

Title			PCI EXPRESS * 16	
Size			GA-H81M-D2V	
Custom			Rev 1.02	
Date:			Friday, November 08, 2013	Sheet 14 of 33

## PCIEX1 SLOT



Gigabyte Technology

PCI EXPRESS X 1 PORT

Title	Document Number	Rev
Size Custom	GA-H81M-D2V	1.02
Date:	Friday, November 08, 2013	Sheet 15 of 33



迅维电脑维修培训学校  
4008009990 www.xinxunwei.com

Gigabyte Confidential  
www.chinafix.com  
Do not Copy

迅维电脑维修培训学校  
4008009990 www.xinxunwei.com

Gigabyte Technology		
Title		
PCI SLOT 1&2		
Size	Document Number	Rev
Custom	GA-H81M-D2V	1.02
Date	Friday, November 08, 2013	Sheet 16 of 33
	2	1



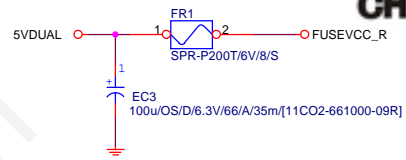




### USB2.0 PWR

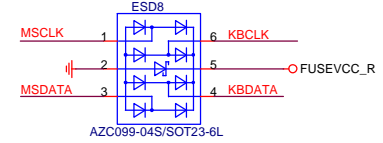
FUSE-0805

KB\_MS\_USB 2-Port 2.0A

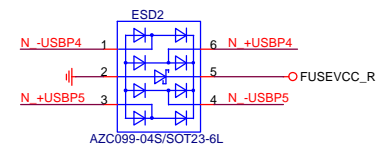


Close to connector

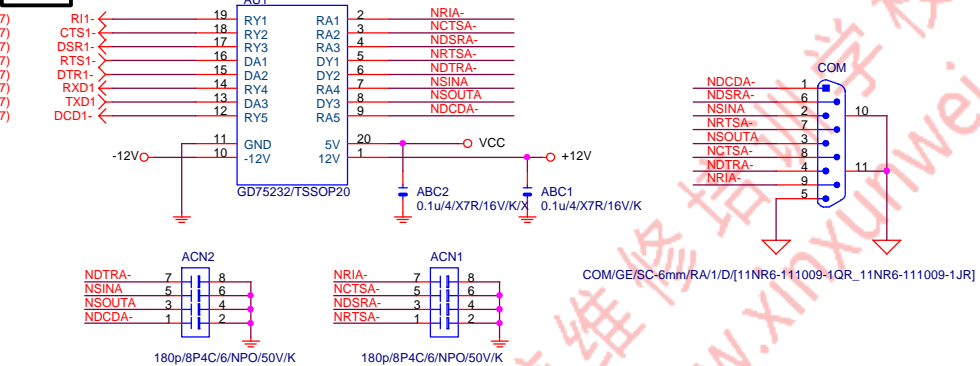
### KB/MS ESD



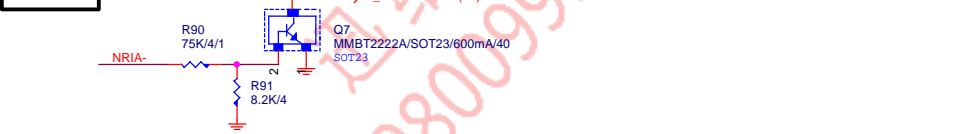
### USB2.0 ESD



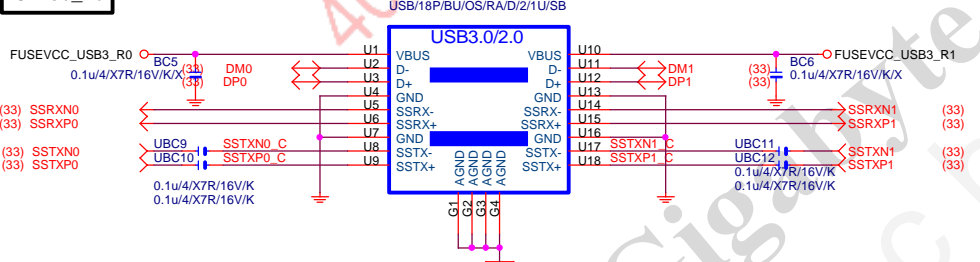
### COM



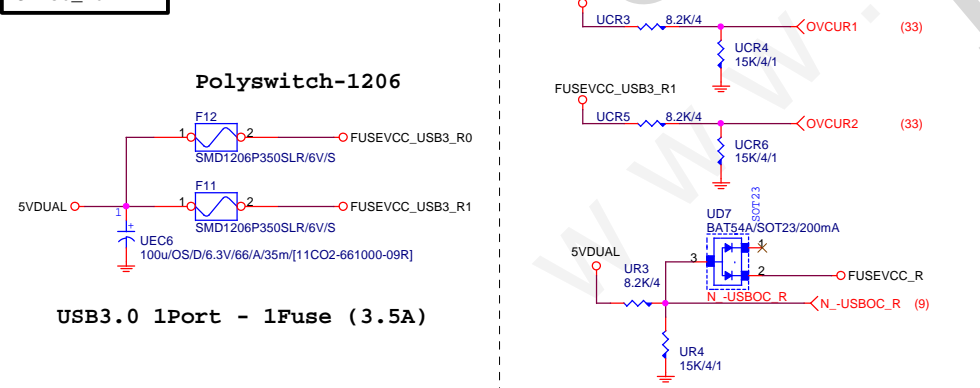
### COM RI



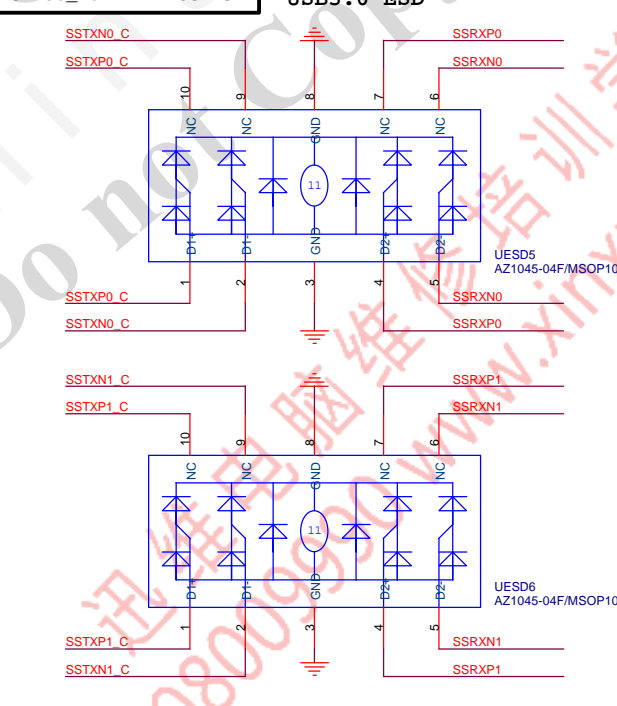
### USB30\_20



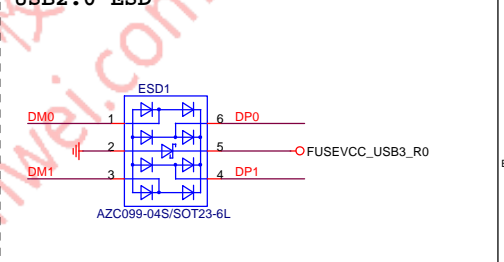
### USB30\_20 PWR



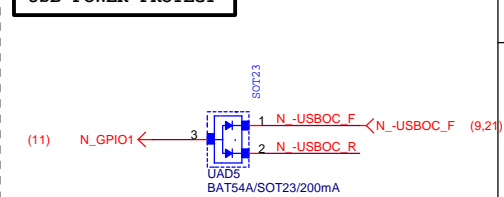
### USB30\_20 ESD PROTECT



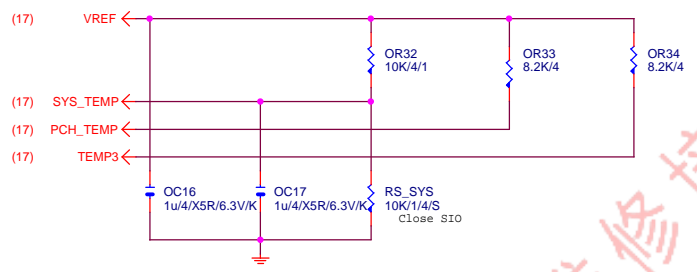
### USB2.0 ESD



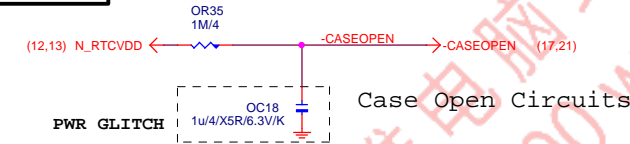
### USB POWER PROTECT



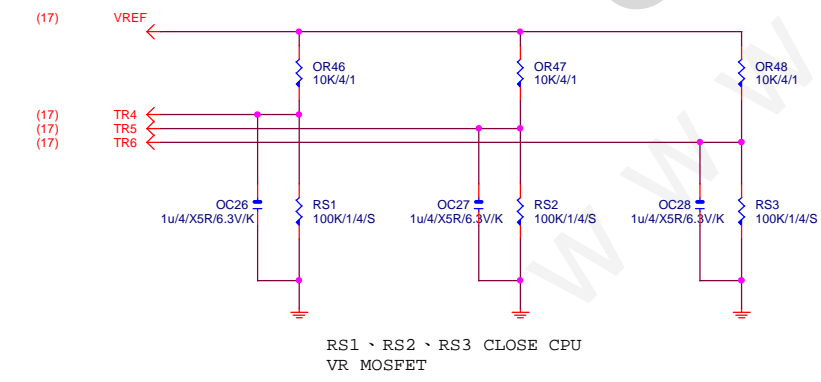
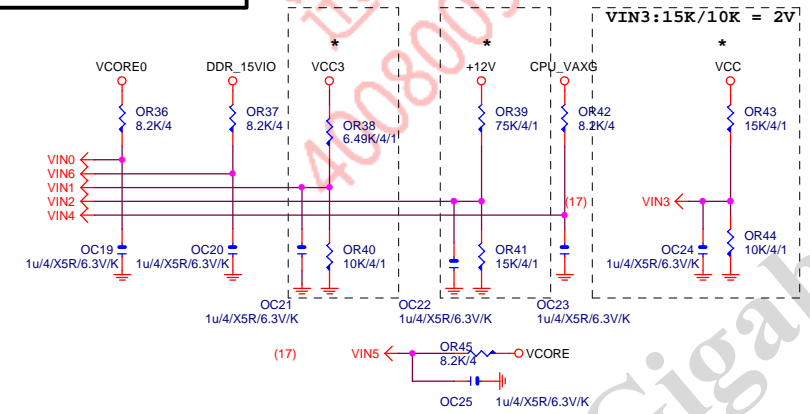
TEMP H/W MONITOR



CASE OPEN

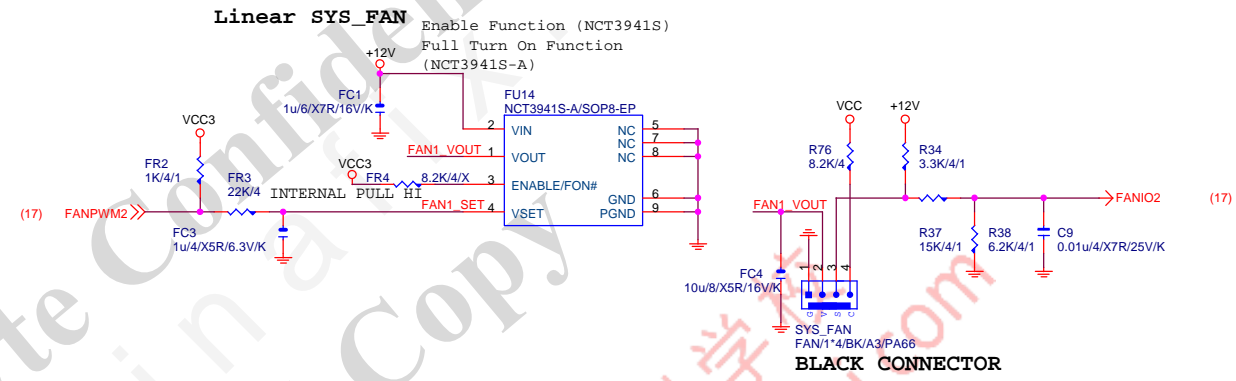


VOLTAGE-- H/W MONITOR

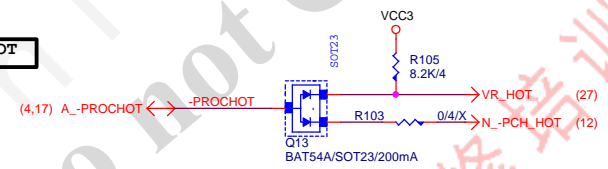


RS1、RS2、RS3 CLOSE CPU VR MOSFET

SYS SMART FAN

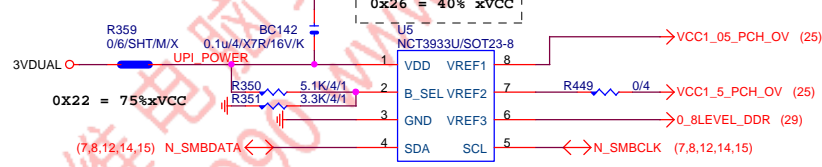


PROHOT



接pwm feedback pin

OV NCT3933



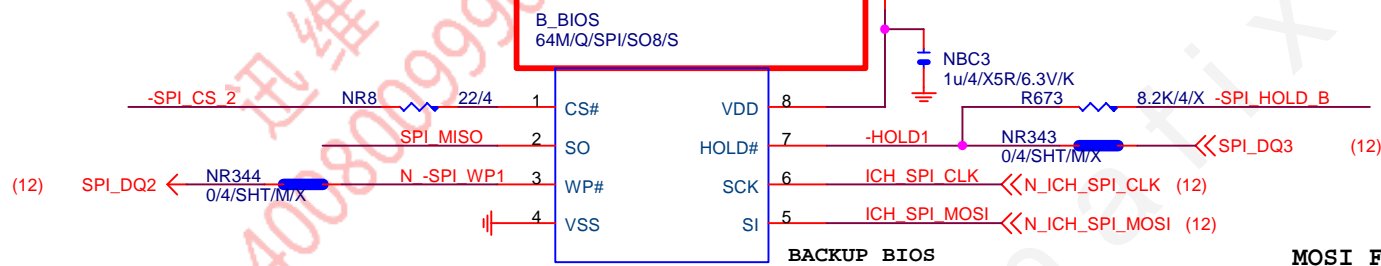
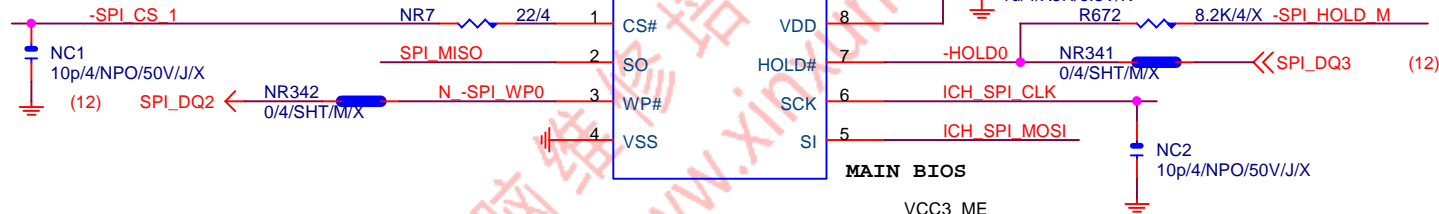
NCT3933	0X2A	0X20	0X22
VREF1	DDRVTT	VREF_DDRA_DQ	PCH Core
VREF2	VREF_DDRA_CA	N/A	VCC1_5_PCH
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	SMREF

**Gigabyte Technology**

Title: HWM,FAN CTRL,OV

Size: Custom Document Number: GA-H81M-D2V Rev: 1.02

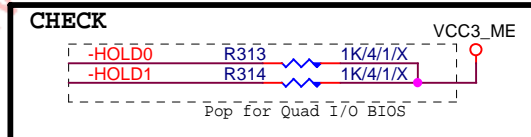
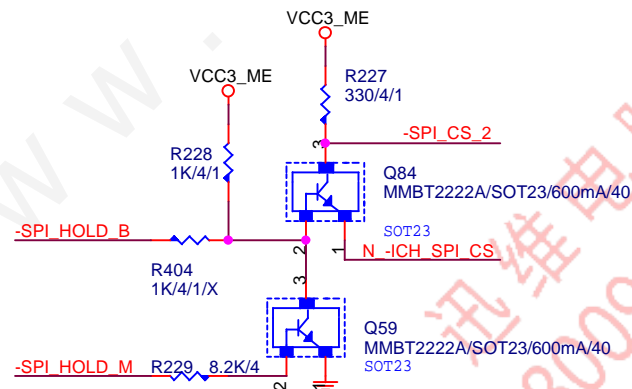
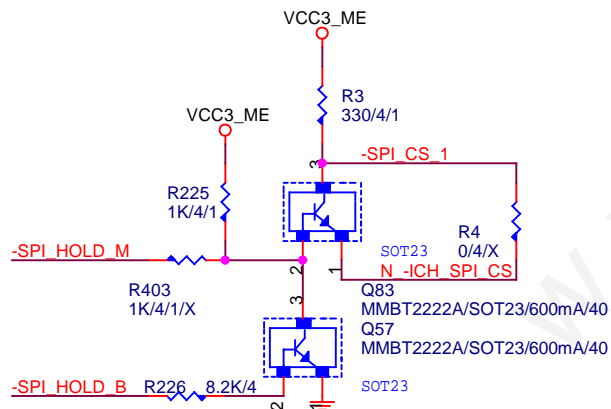
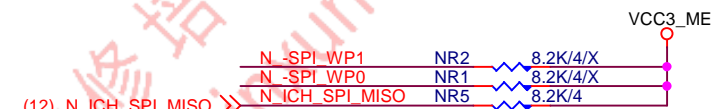
Date: Friday, November 08, 2013 Sheet: 19 of 33



BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating  
0 means PD 1K

#### MOSI For DMI RX Termination Voltage



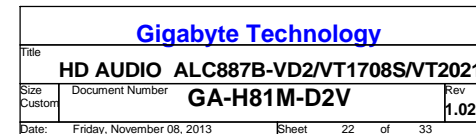
**Gigabyte Technology**

Title			DUAL BIOS
Size Custom	Document Number	GA-H81M-D2V	
Date:	Friday, November 08, 2013	Sheet	20 of 33
		Rev	1.02



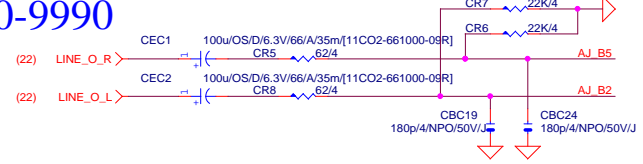
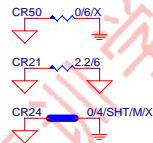


	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O





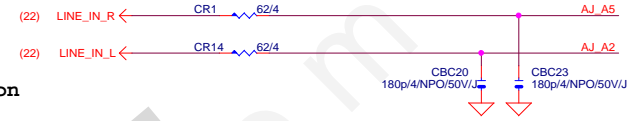
www.xinxunwei.com 400-800-9990



### LINE-IN

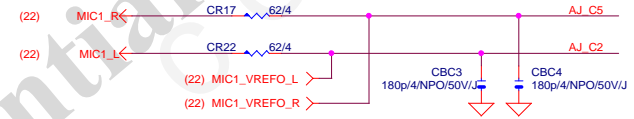
Verify MIC function  
in LINE-in

Only reserved for ALC888

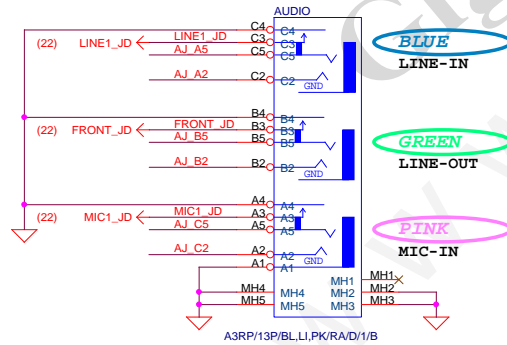


For 889A/888

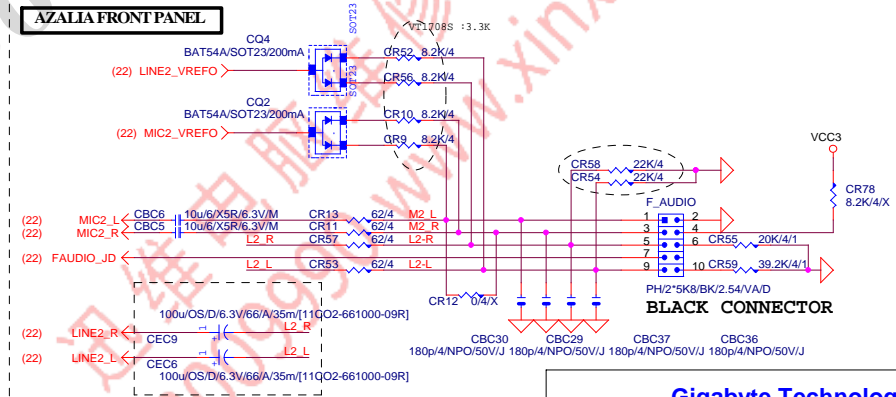
### MIC-IN



### SPDIF\_OUT

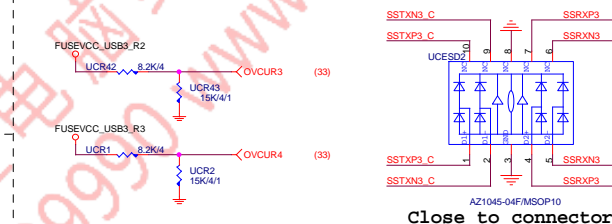
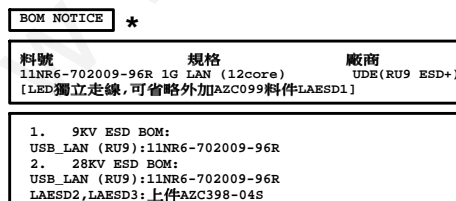
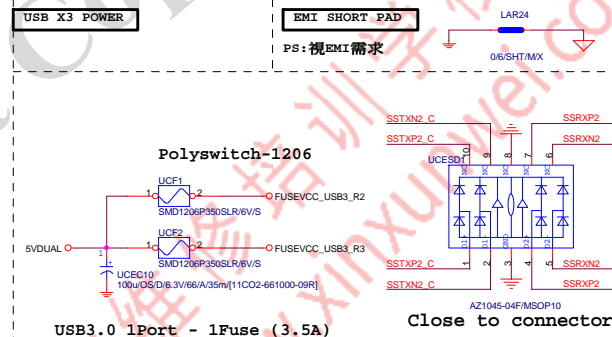
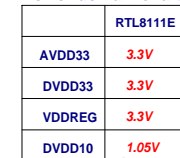


### AZALIA FRONT PANEL

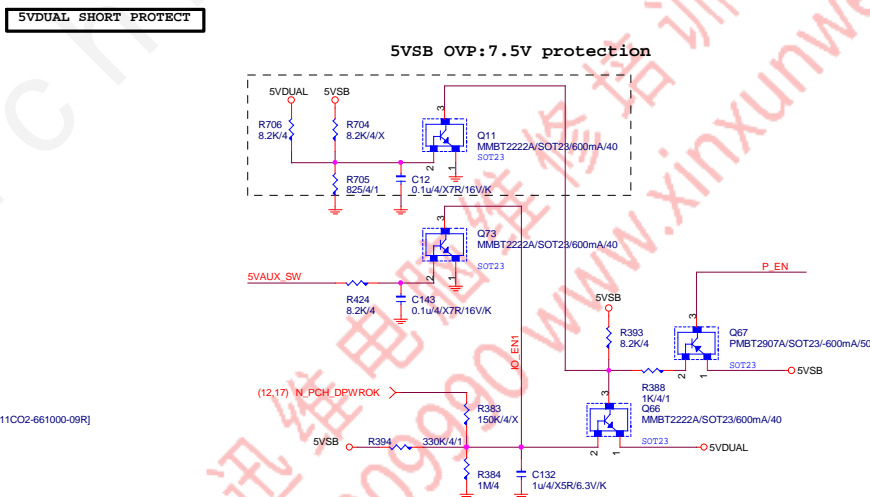
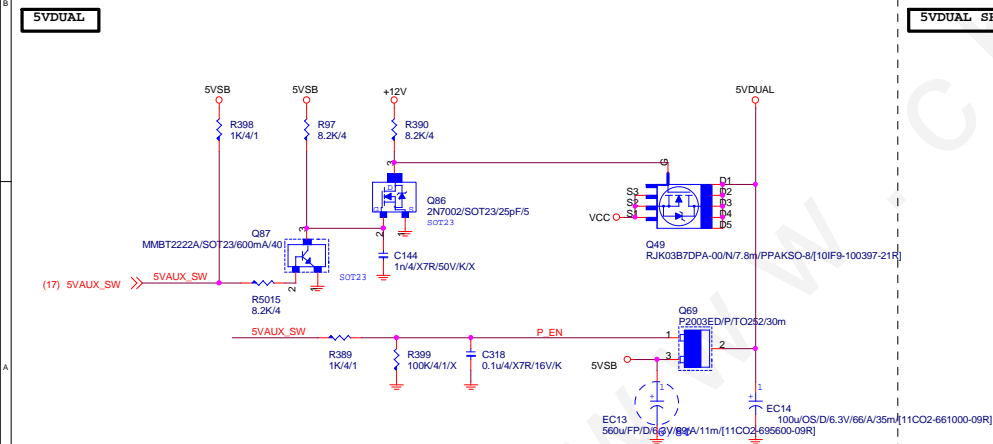
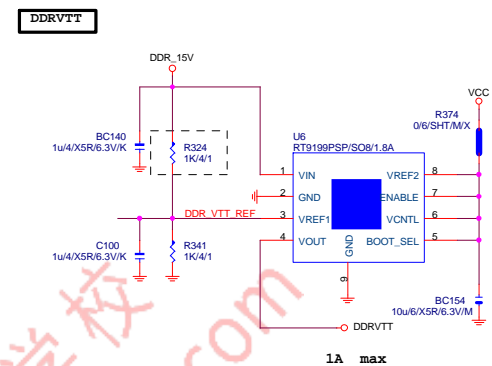
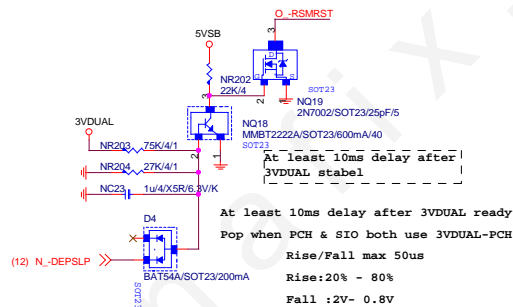
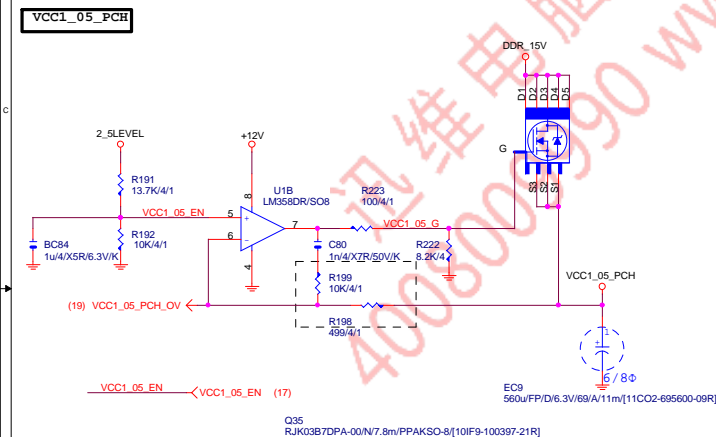
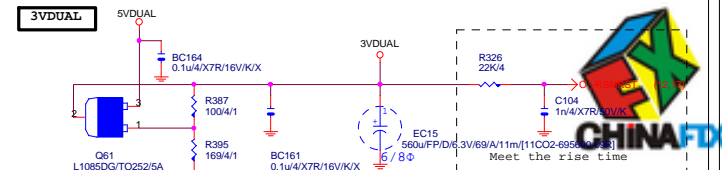
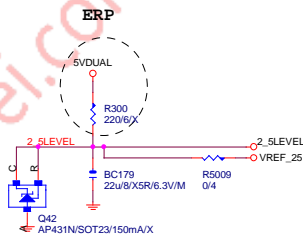
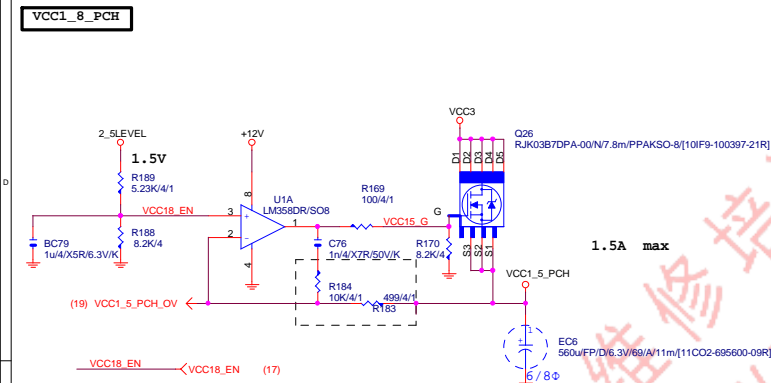


Gigabyte Technology

Title			AUDIO JACK
Size			GA-H81M-D2V
Date:			Friday, November 08, 2013
Sheet			23 of 33
Rev			1.02



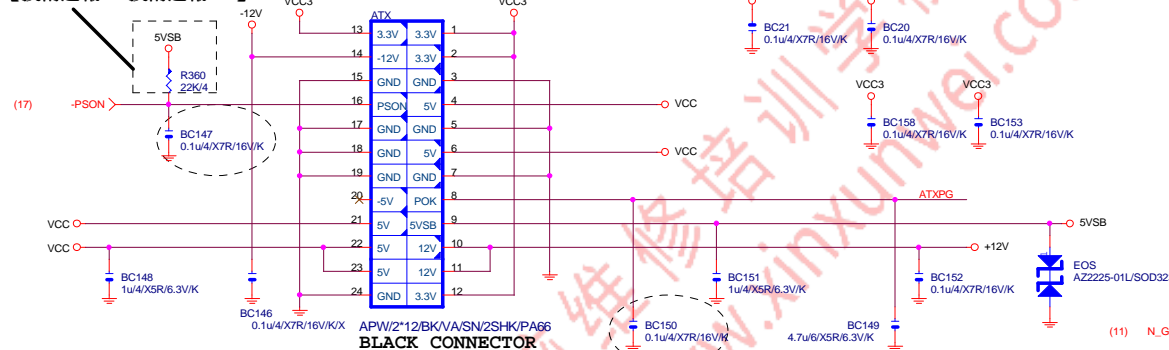




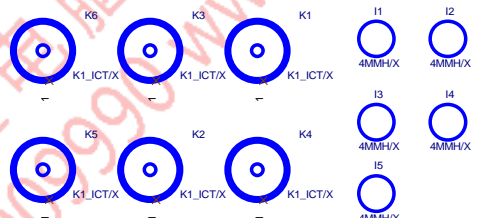
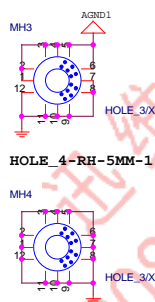
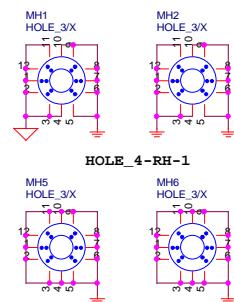
<b>Gigabyte Technology</b>			
Title			
<b>DISCRETE POWER</b>			
Size	Document Number		Rev
Custom	<b>GA-H81M-D2V</b>		<b>1.02</b>
Date:	Friday, November 08, 2013	Sheet	25 of 33

# ATXX24 POWER CONNECTOR

【技術通報R&D技術通報155】



BLACK CONNECTOR



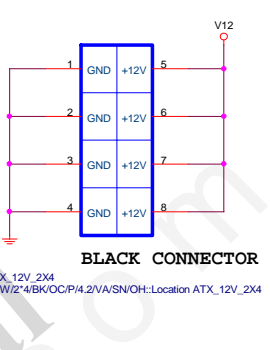
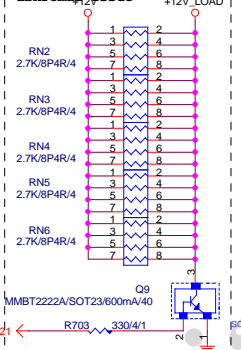
To prevent the 5VSB under loading when boot

## TPM

www.xinxunwei.com 4008009990

# ATXX4 POWER CONNECTOR

【技術通報R&D技術通報158】

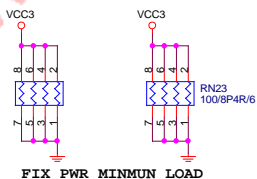
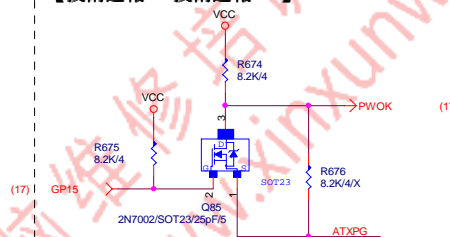


BLACK CONNECTOR

ATX\_12V\_2X4  
APW/2\*4BK/OC/P/4.2/VA/SN/OH:Location ATX\_12V\_2X4

## PWOK PATCH

【技術通報R&D技術通報154】



FIX PWR MINMUN LOAD

Gigabyte Technology

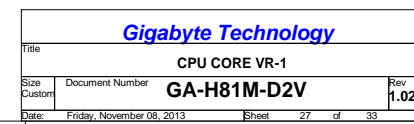
ATX CONNECTOR

GA-H81M-D2V

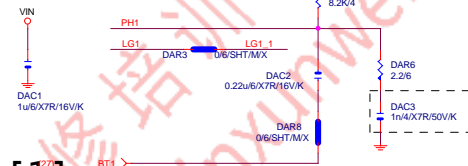
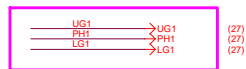
Rev 1.02

Date: Friday, November 08, 2013 Sheet 26 of 33

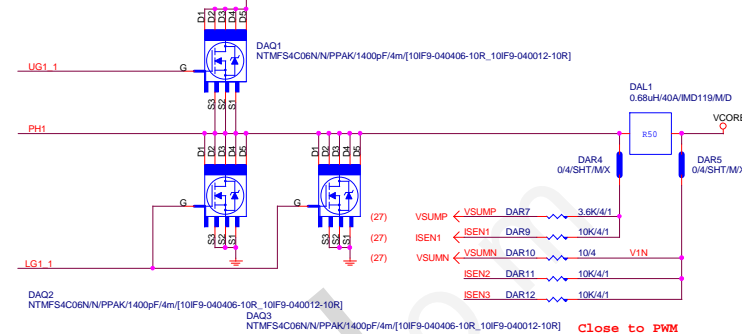




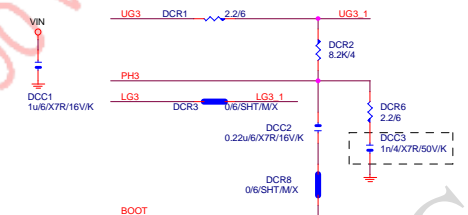
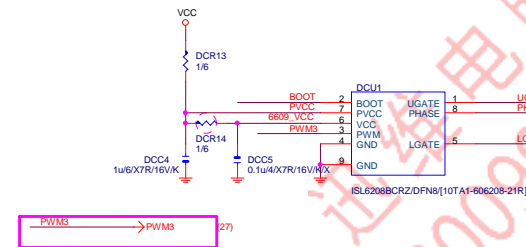
PHASE 1



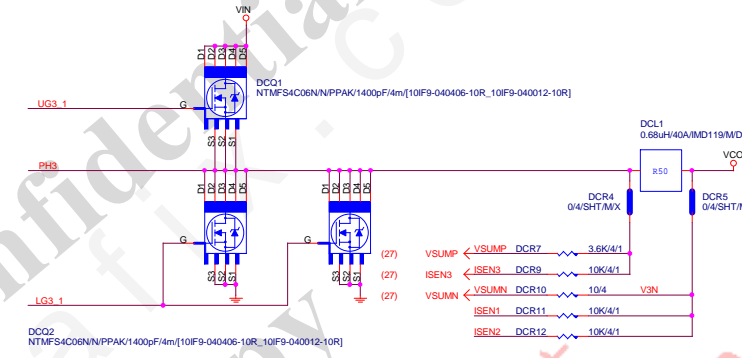
[1]



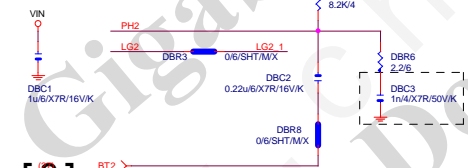
PHASE 3



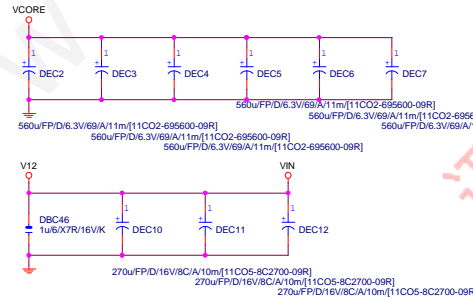
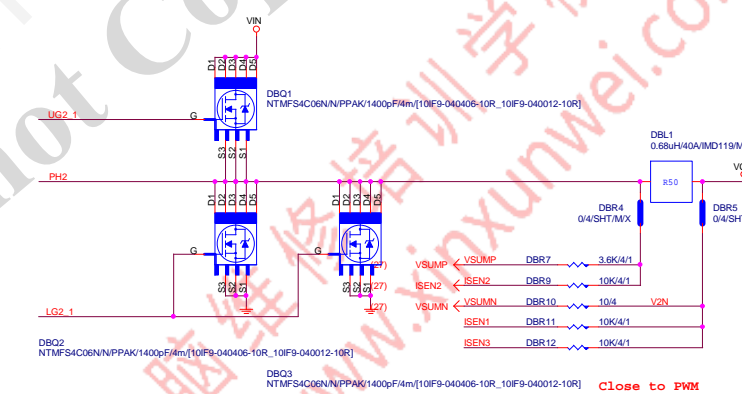
[3]



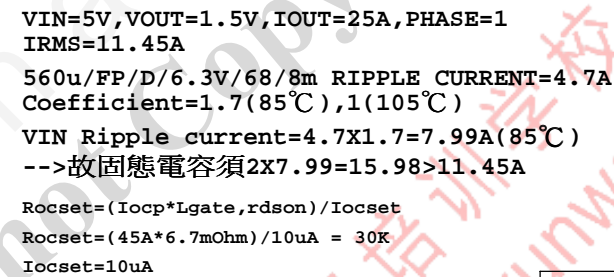
PHASE 2



[2]







<b><i>Gigabyte Technology</i></b>			
Title			
<b>DDR POWER</b>			
Size	Document Number	<b>GA-H81M-D2V</b>	Rev
Custom			<b>1.02</b>
Date:	Friday, November 08, 2013	Sheet	29 of 33

VCC1\_05\_ME

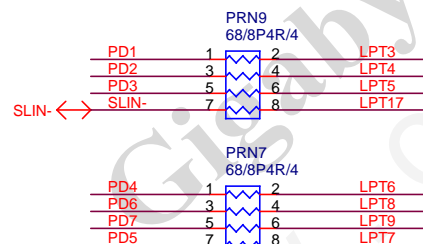
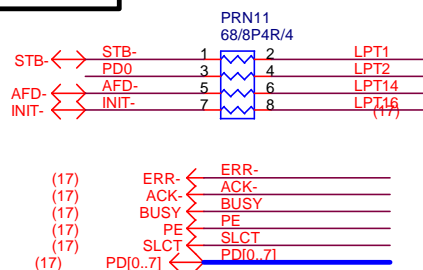
【技術通報R&D技術通報156】  
(RICHTER), (NUVOTON), (EMC)做共用  
PIN7分壓阻值須做修改為100K以上電阻值

www.xinxunwei.com 400-800-9990

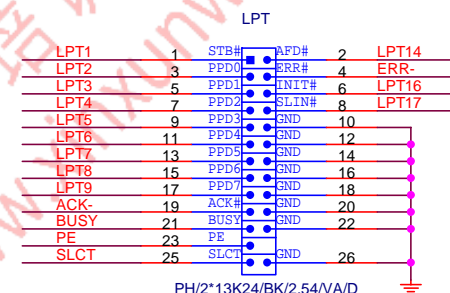
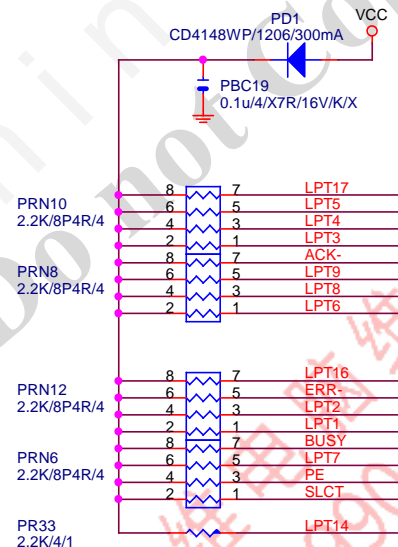
VCC3\_ME



# LPT PORT



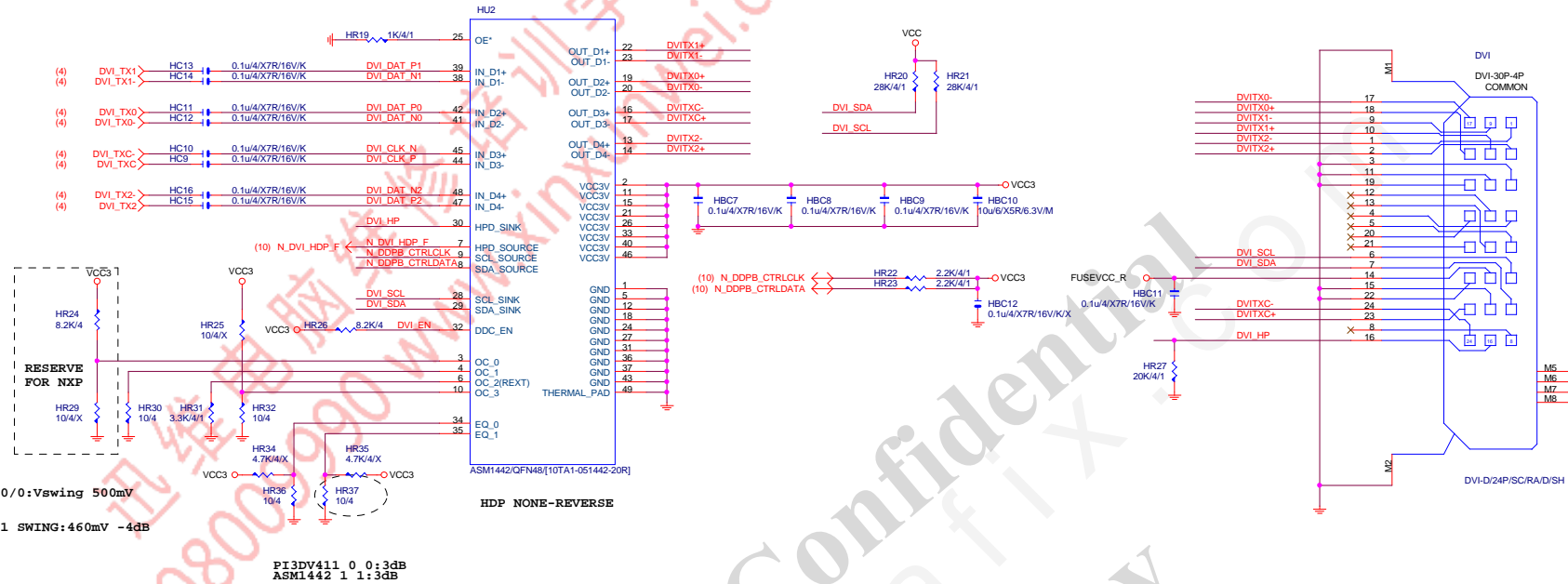
【技術通報R&D技術通報151】  
33ohm Change to 68ohm



Gigabyte Technology

Title			
LPT			
Size Custom	Document Number	GA-H81M-D2V	Rev 1.02
Date:	Friday, November 08, 2013	Sheet 30 of 33	

## DVI LEVEL SHIFT



## HDMI LEVEL SHIFT



迅维电脑维修培训学校  
4008009990 www.xinxunwei.com

www.chinafix.com

迅维电脑维修培训学校  
4008009990 www.xinxunwei.com

Gigabyte Technology			
Title			
ITE IT8892E			
Size	Document Number	Rev	
Custom	GA-H81M-D2V	1.02	
Date:	Friday, November 08, 2013	Sheet	32 of 33



